



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ramsbey, et al.

Serial No.: 09/430,366

Filed: October 28, 1999

For: **Method Of Making A Memory Cell  
With Polished Insulator Layer**

Art Unit: 2813

Examiner: Chen, Jack S. J.

#30/G

9/11/03

Norm H.

**AMENDMENT AND RESPONSE TO OFFICE ACTION**

Honorable Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the Office Action dated May 20, 2003 in the above-referenced patent application. Please enter and consider the following amendments and remarks.

RECEIVED  
AUG 25 2003  
TECHNOLOGY CENTER 2800